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Substitute for form 1449A/PTO				Complete if Known	
<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>				Application Number	<u>10/065,340</u>
				Filing Date	<u>10/06/2002</u>
				First Named Inventor	<u>MELVIN</u>
				Art Unit	
				Examiner Name	
Sheet	<u>1</u>	of	<u>3</u>	Attorney Docket Number	

Examiner Signature		Date Considered	
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\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

<sup>1</sup> Applicant's unique citation designation number (optional). <sup>2</sup> See Kind Codes of USPTO Patent Documents at [www.uspto.gov](http://www.uspto.gov) or MPEP 901.04. <sup>3</sup> Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). <sup>4</sup> For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. <sup>5</sup> Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. <sup>6</sup> Applicant is to place a check mark here if English language Translation is attached.

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Substitute for form 1449B/PTO		Complete if Known	
<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>		Application Number	<i>10/065,340</i>
(use as many sheets as necessary)		Filing Date	<i>10/06/2002</i>
		First Named Inventor	<i>MELVIN</i>
		Group Art Unit	
		Examiner Name	
Sheet	<i>2</i>	of	<i>3</i>
		Attorney Docket Number	

#### **OTHER PRIOR ART -- NON PATENT LITERATURE DOCUMENTS**

**Examiner  
Signature** \_\_\_\_\_ **Date  
Considered** \_\_\_\_\_

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## INFORMATION DISCLOSURE STATEMENT BY APPLICANT - Page 3 of 3

Application Number: 10/065,340

Filing Date: 10/06/2002

Applicant: Stephen Waller Melvin

### Cite Non Patent Publication

- 1 S. MELVIN and Y. PATT, "Handling of Packet Dependencies: A Critical Issue for Highly Parallel Network Processors," *International Conference on Compilers, Architecture, and Synthesis for Embedded Systems*, October 8-11, 2002, Grenoble, France
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- 5 J. G. STEFFAN AND T. MOWRY, "The potential for using thread-level data speculation to facilitate automatic parallelization," *Proceedings of the Fourth International Symposium on High-Performance Computer Architecture*, Las Vegas, February, 1998.
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